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istic never is better than -1, so the system never has any gain to compensate for noise. Exercise 1.81 The circuit functions as a buffer with logic levels $V_{IL} = 1.5$; $V_{IH} = 1.8$; $V_{OL} = 1.2$; $V_{OH} = 3.0$. It can receive inputs from LVCMOS and LVTTTL gates be- ... Digital Design and Computer Architecture, Exercise Solutions. SOLUTIONS = + + ...

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